



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: **Marotta et al.**

Serial No.: **10/692,272**

Filed: **October 22, 2003**

Docket No.: **TI-18913R**

Examiner: **C.P. Chaudhari**

Art Unit: **2891**

Conf. No.: **7712**

For: **METHOD OF MAKING FLOATING-GATE MEMORY-CELL ARRAY
WITH DIGITAL LOGIC TRANSISTORS**

TRANSMITTAL OF FORMAL DRAWINGS

June 22, 2005

Commissioner For Patents
Attention: Official Drafts Person
P.O. Box 1450
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail on the date below in an envelope addressed to: Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Ivonne Olivares

6/29/05
Date

Dear Sir:

Submitted herewith is an amended formal drawing sheet under 37 CFR 1.173 (b)(3) in response to the Office Action dated May 12, 2005.

This enclosed drawing is being provided in a timely manner, therefore no additional fee is necessary.

Charge any necessary fee to the deposit account of Texas Instruments Incorporated, Account 20-0668. This authorization is enclosed in triplicate.

Respectfully submitted,



Frank Cimino
Attorney for Applicants
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